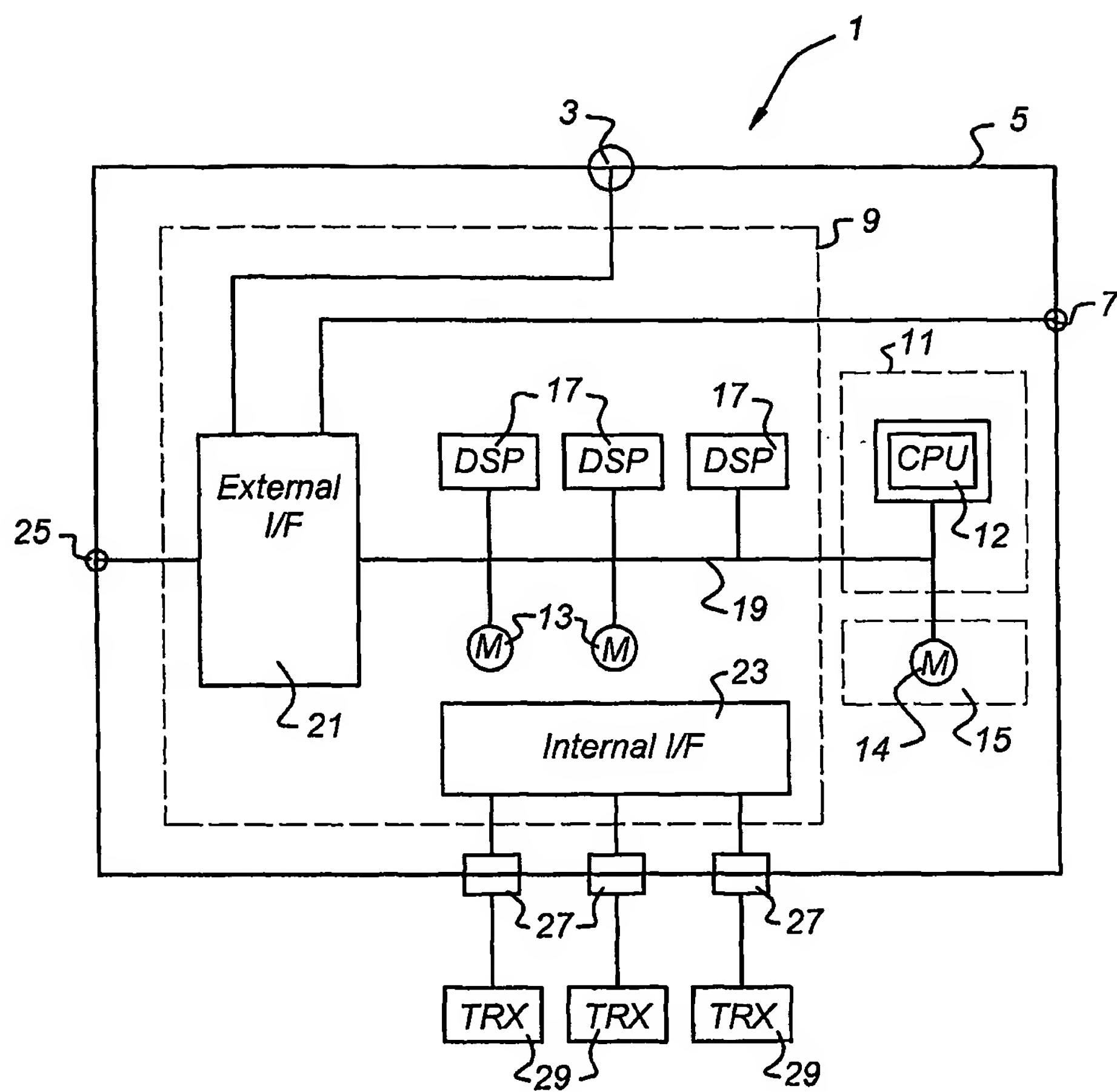


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Fig 1 Prior art

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Fig 2

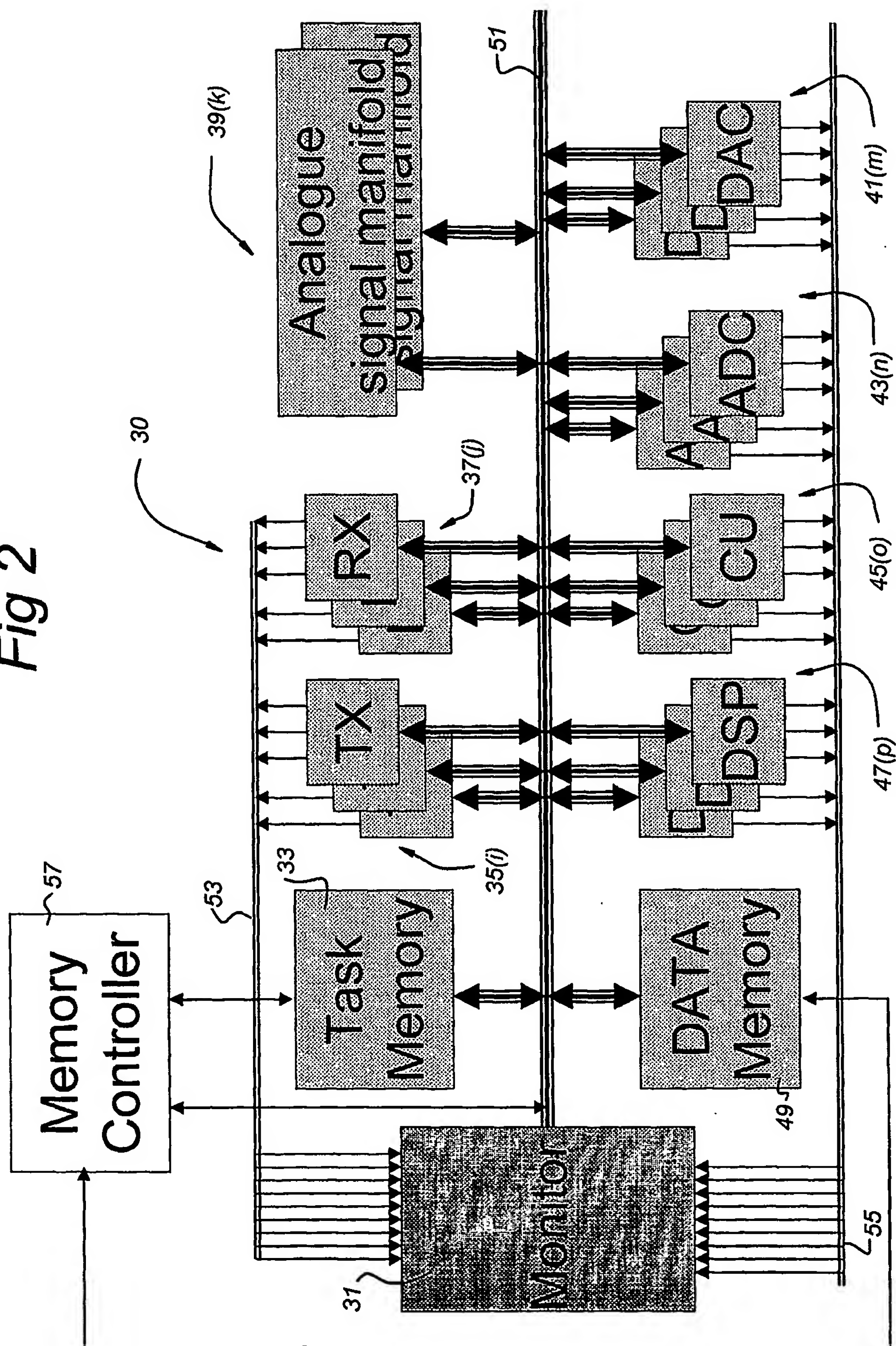


Fig 3

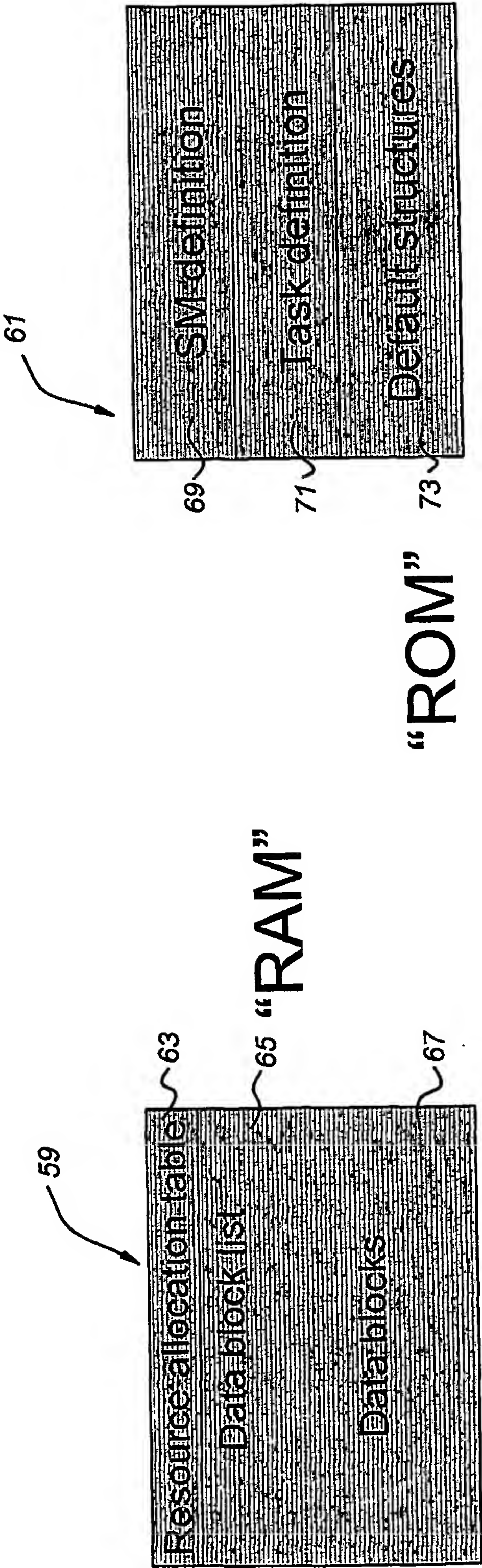
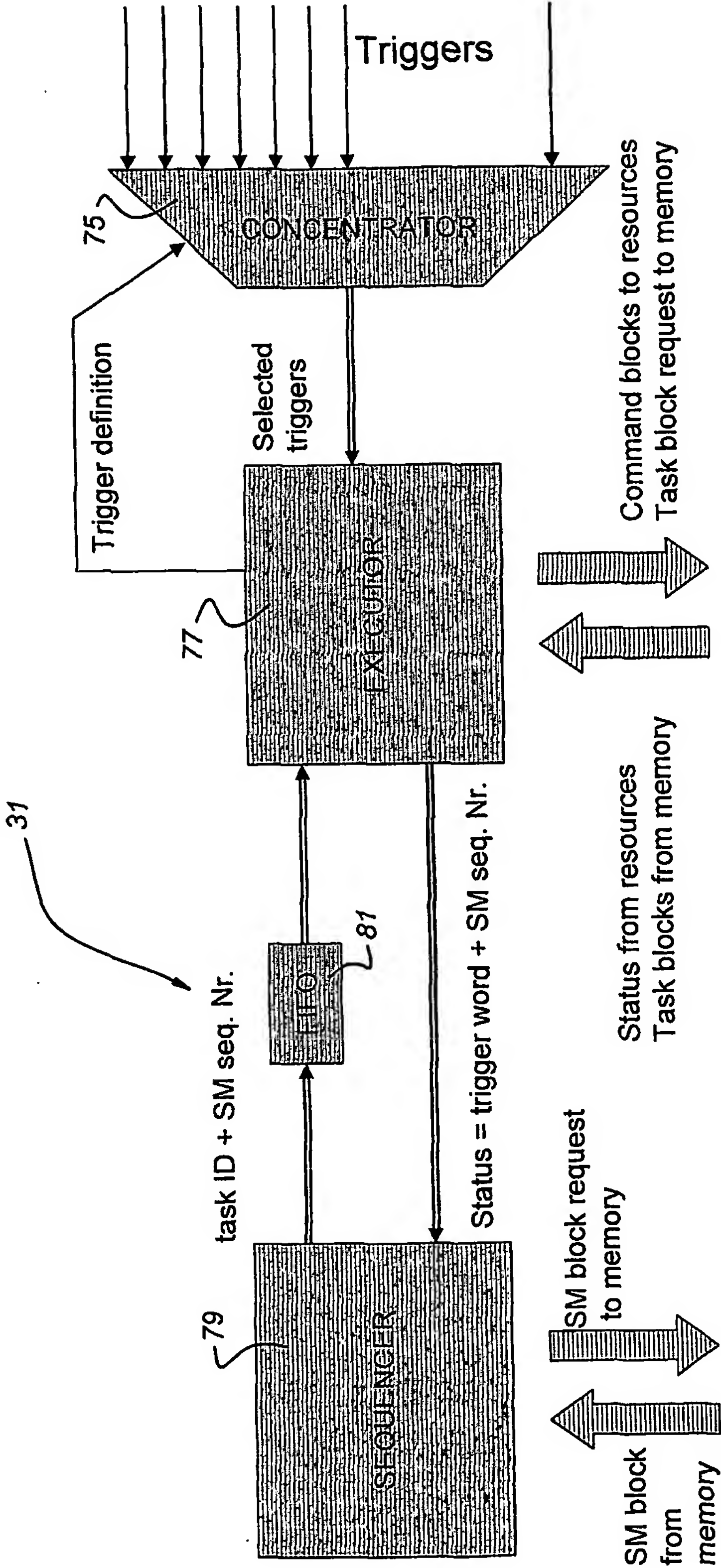
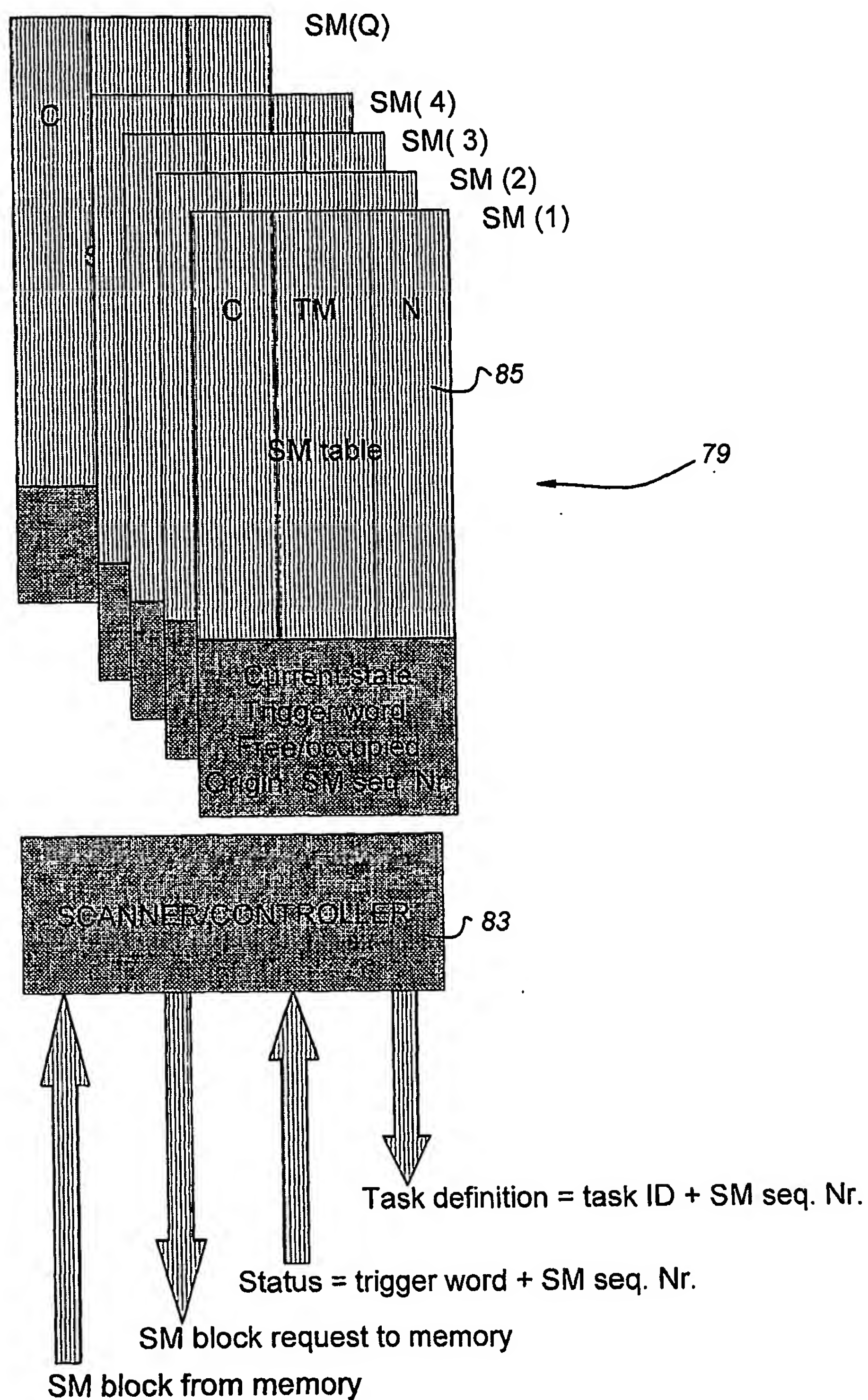


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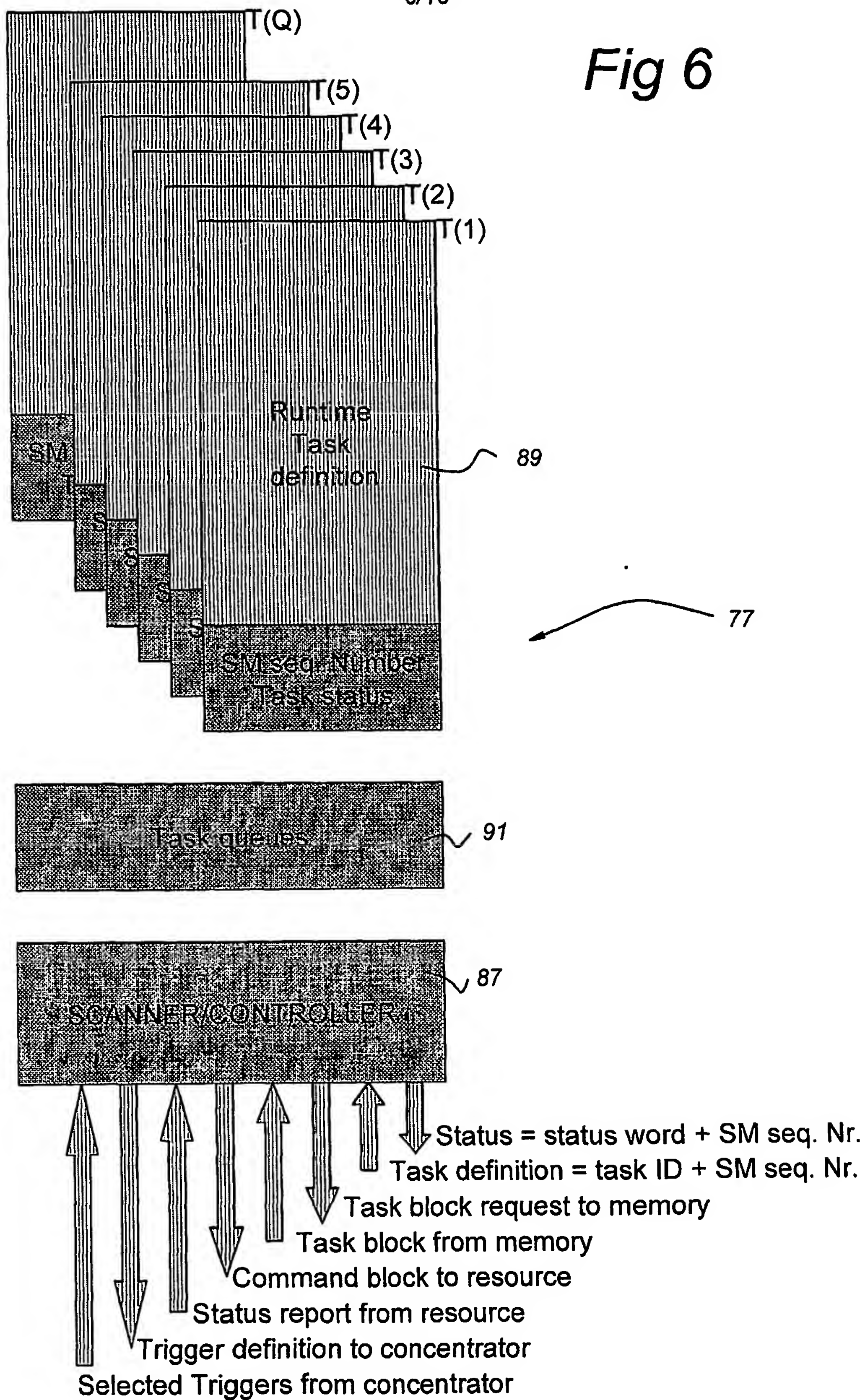


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Fig 5

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Fig 6



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Fig 7

SMXXXX.DOC

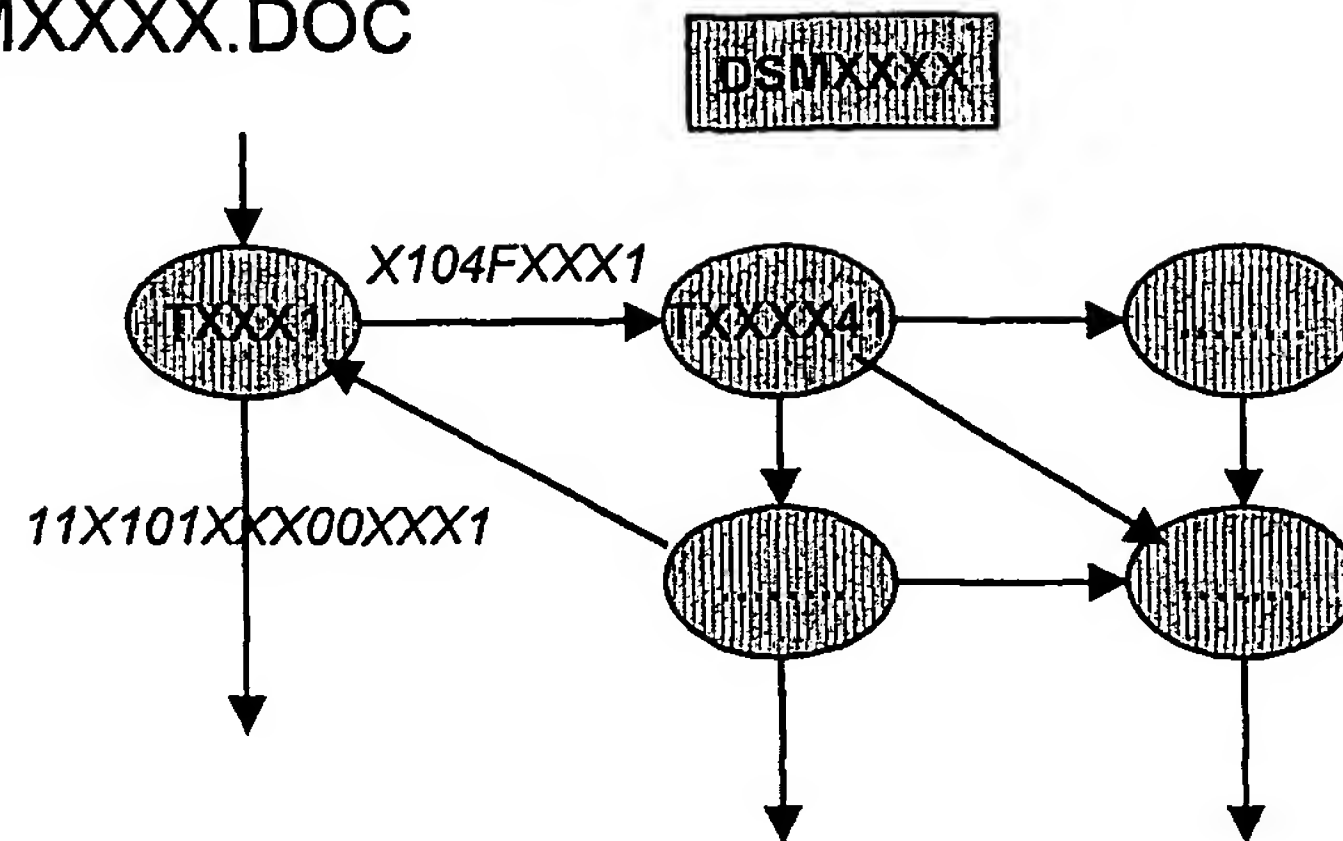
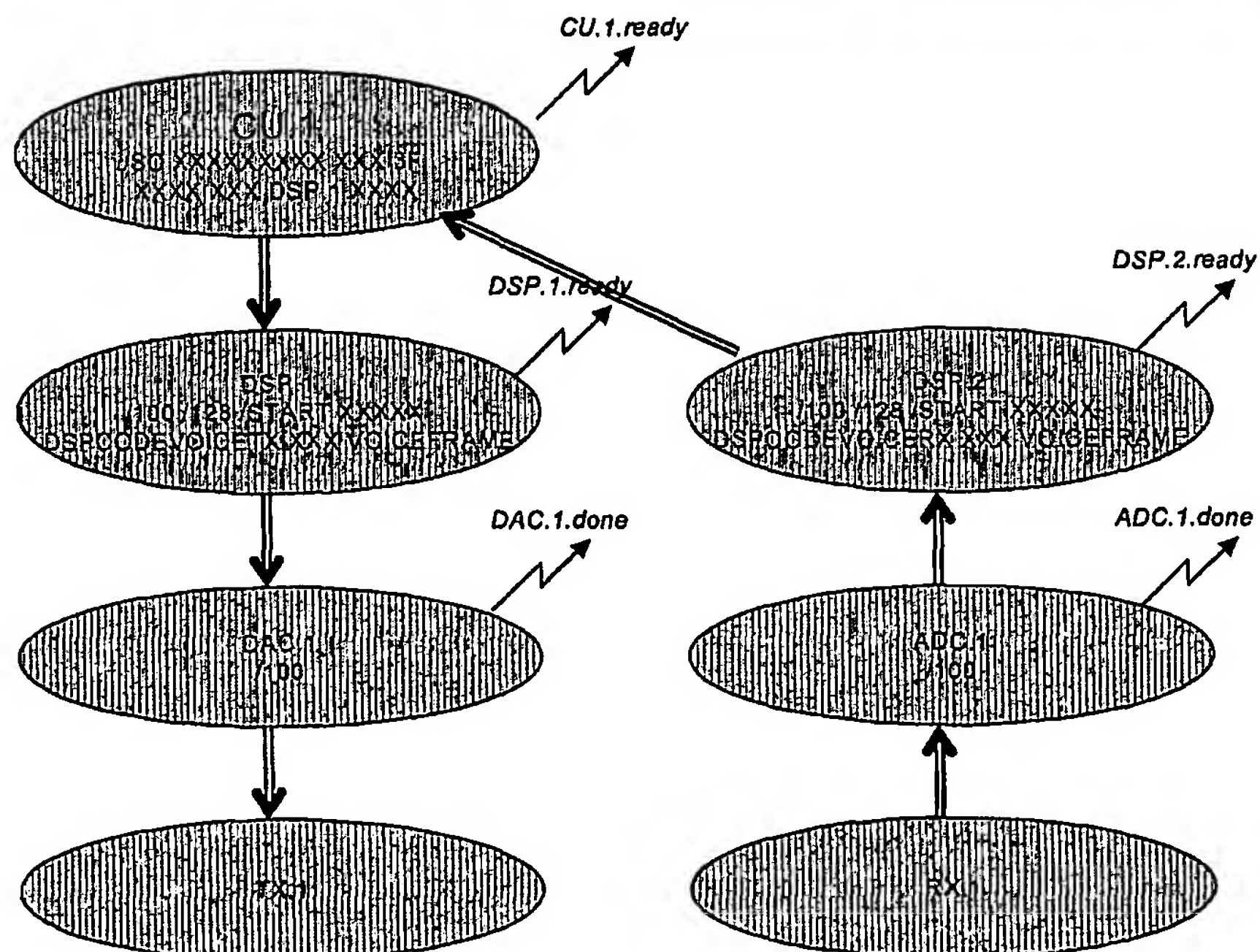


Fig 8

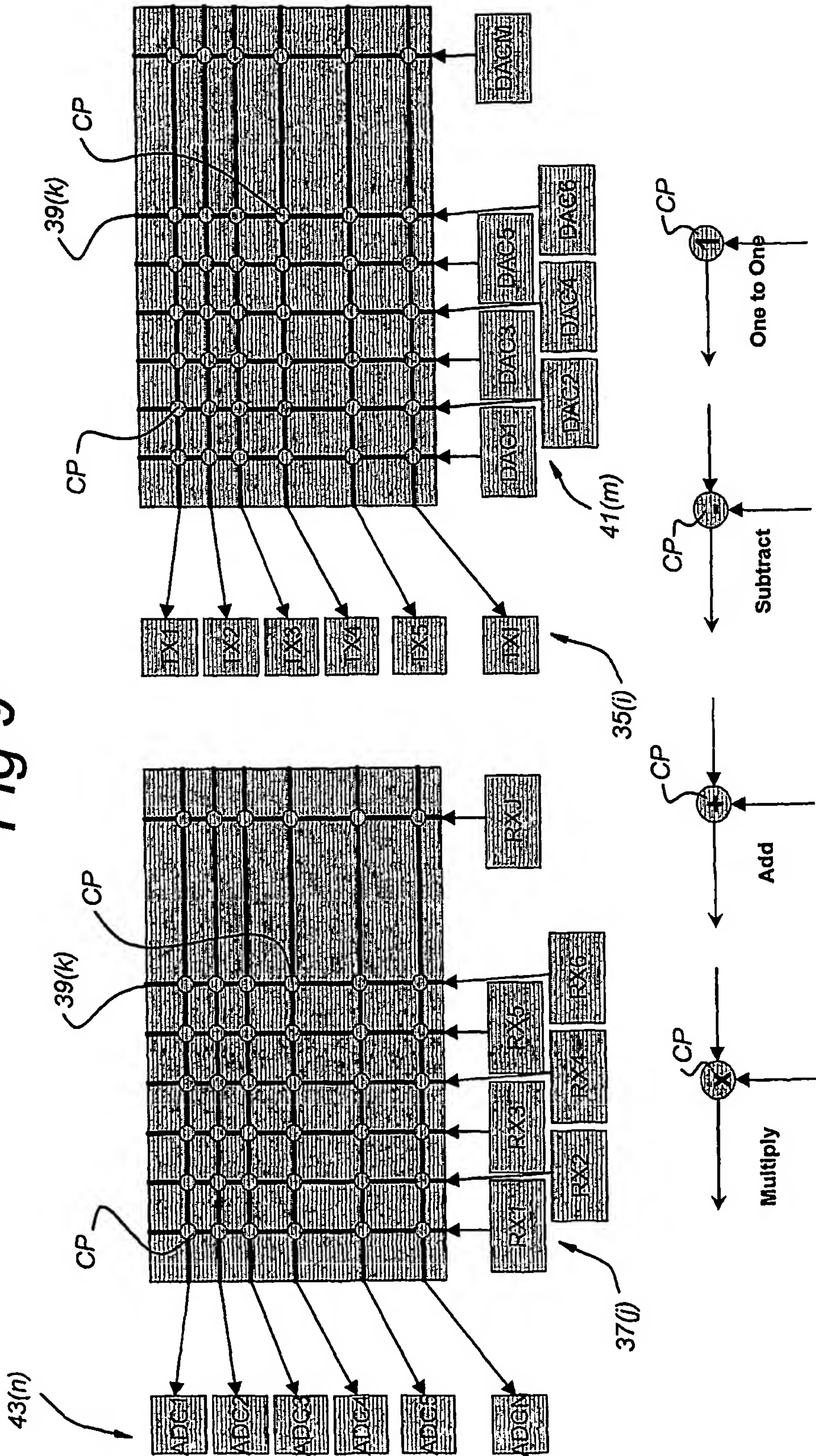
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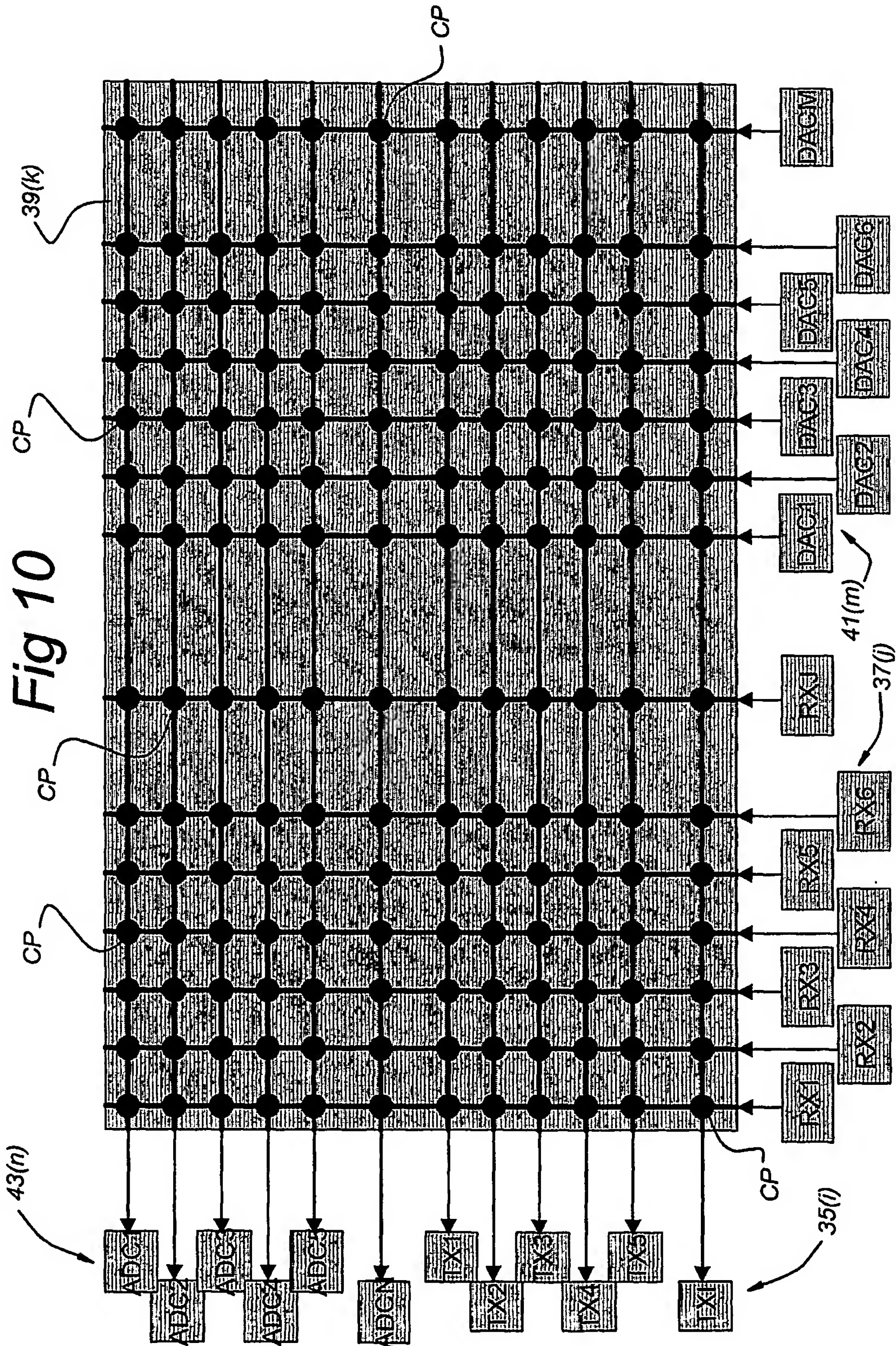


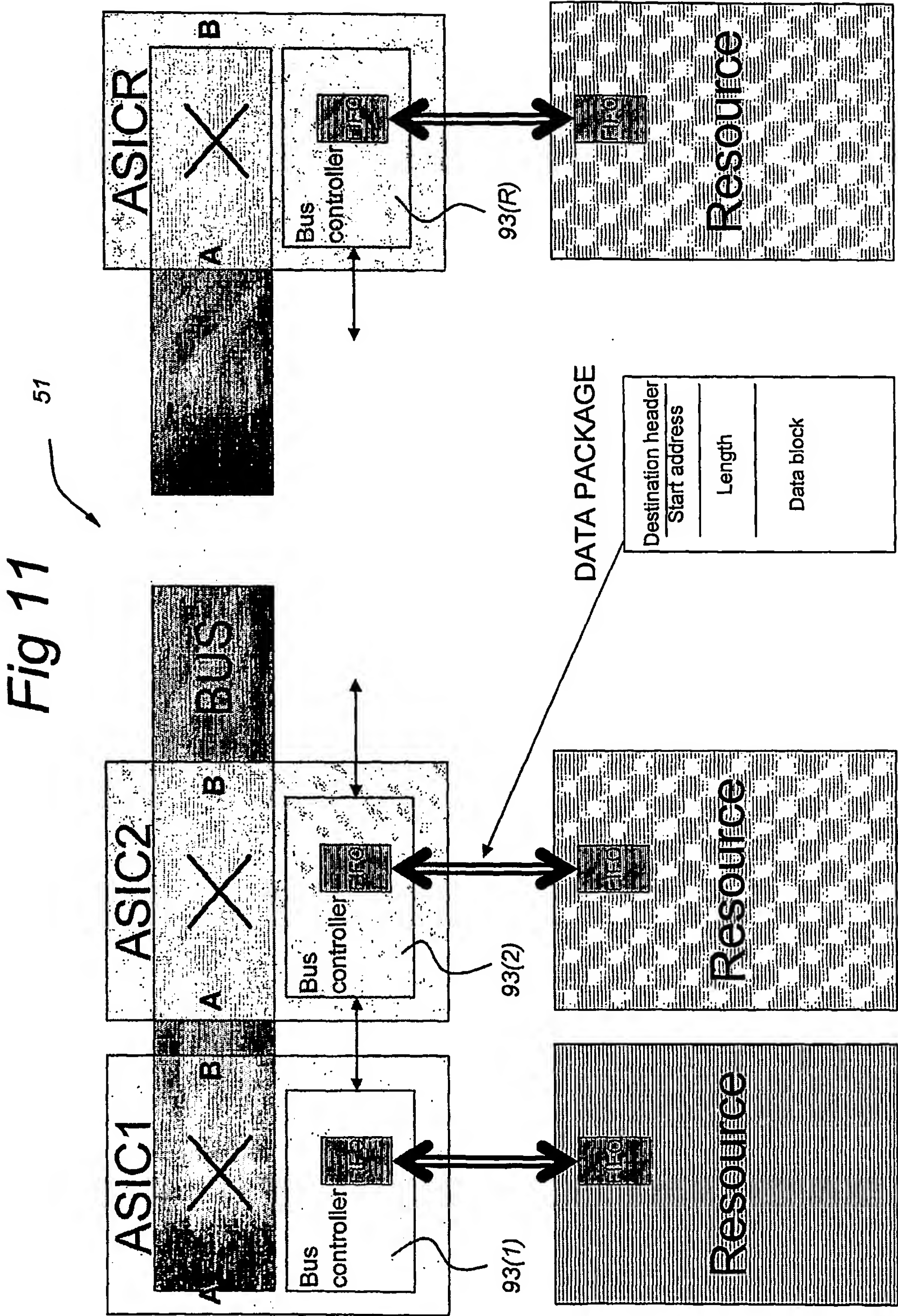
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 DSP.1.status,DSP.2.status

Fig 9







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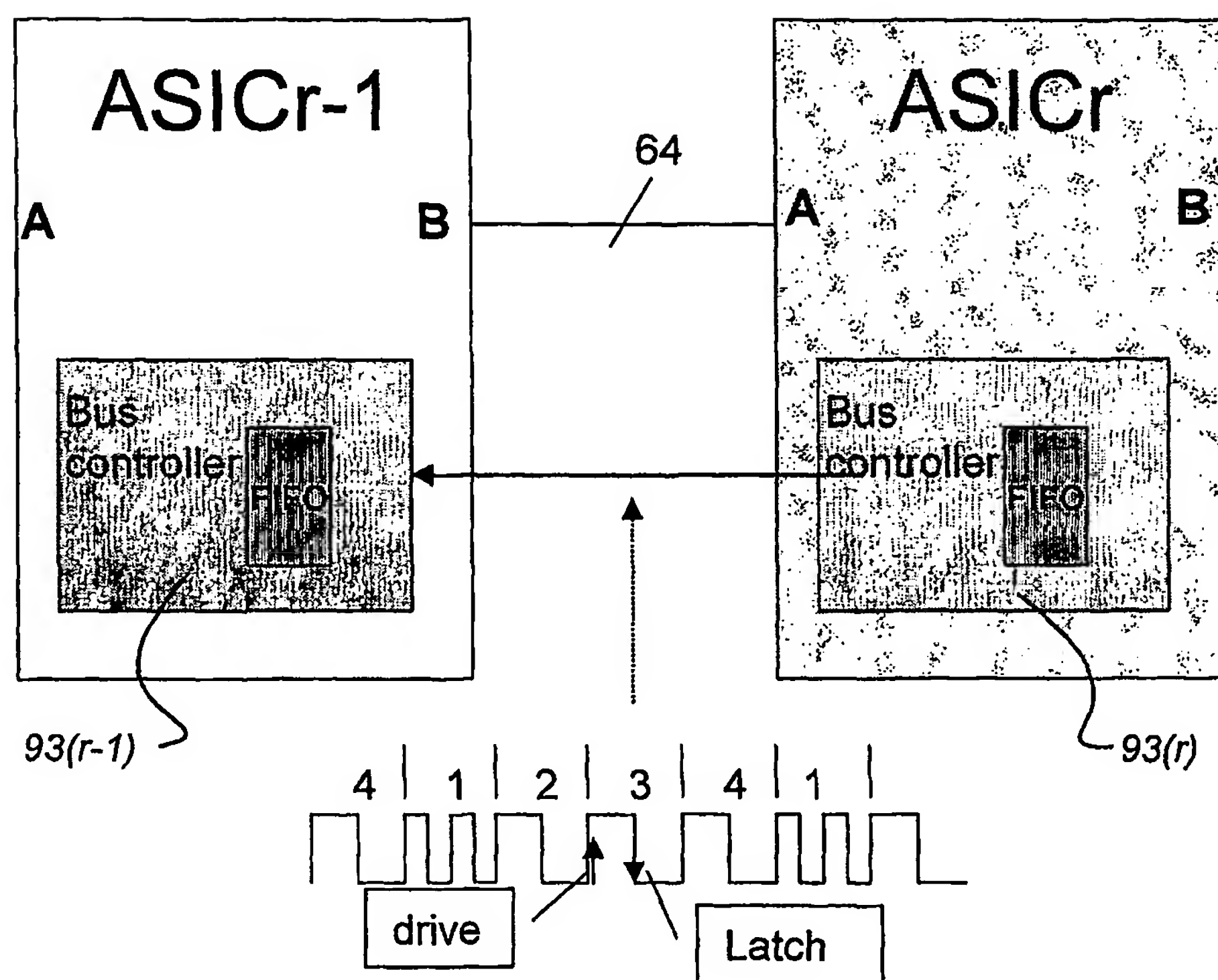
Fig 12

Fig 13

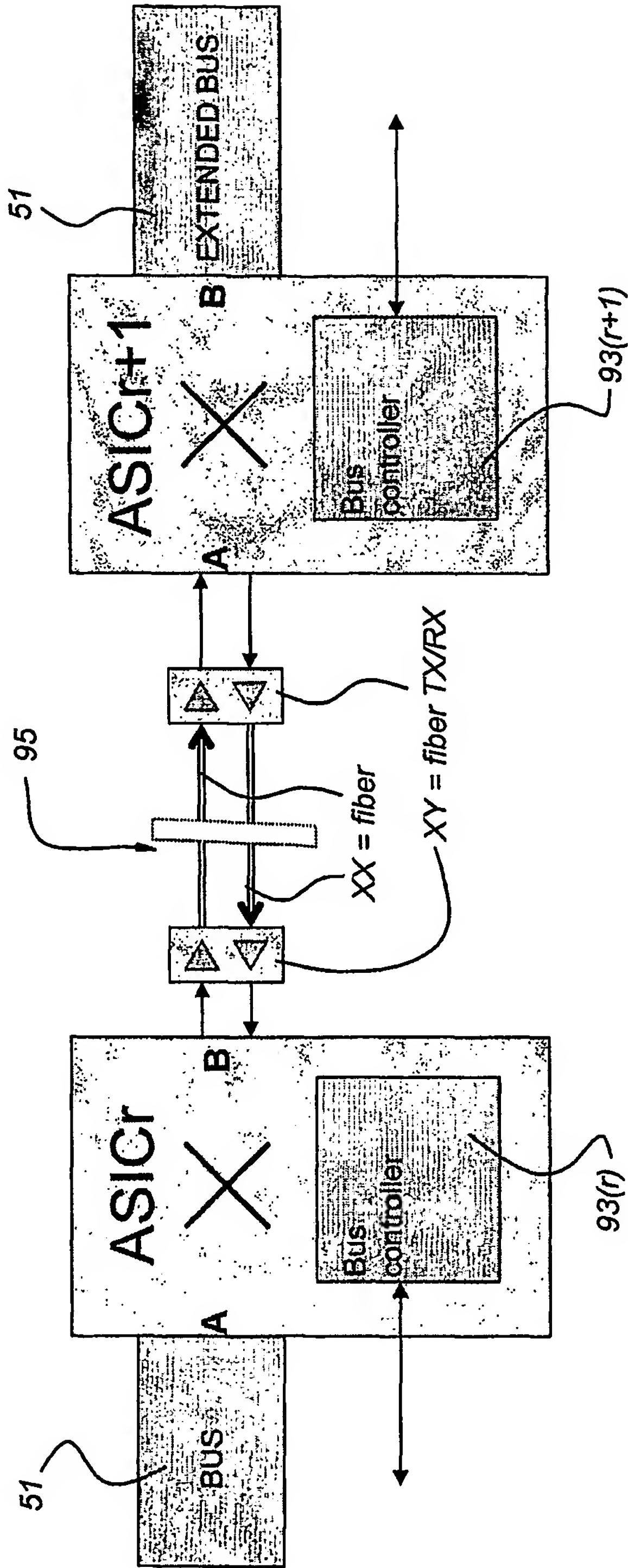


Fig 14

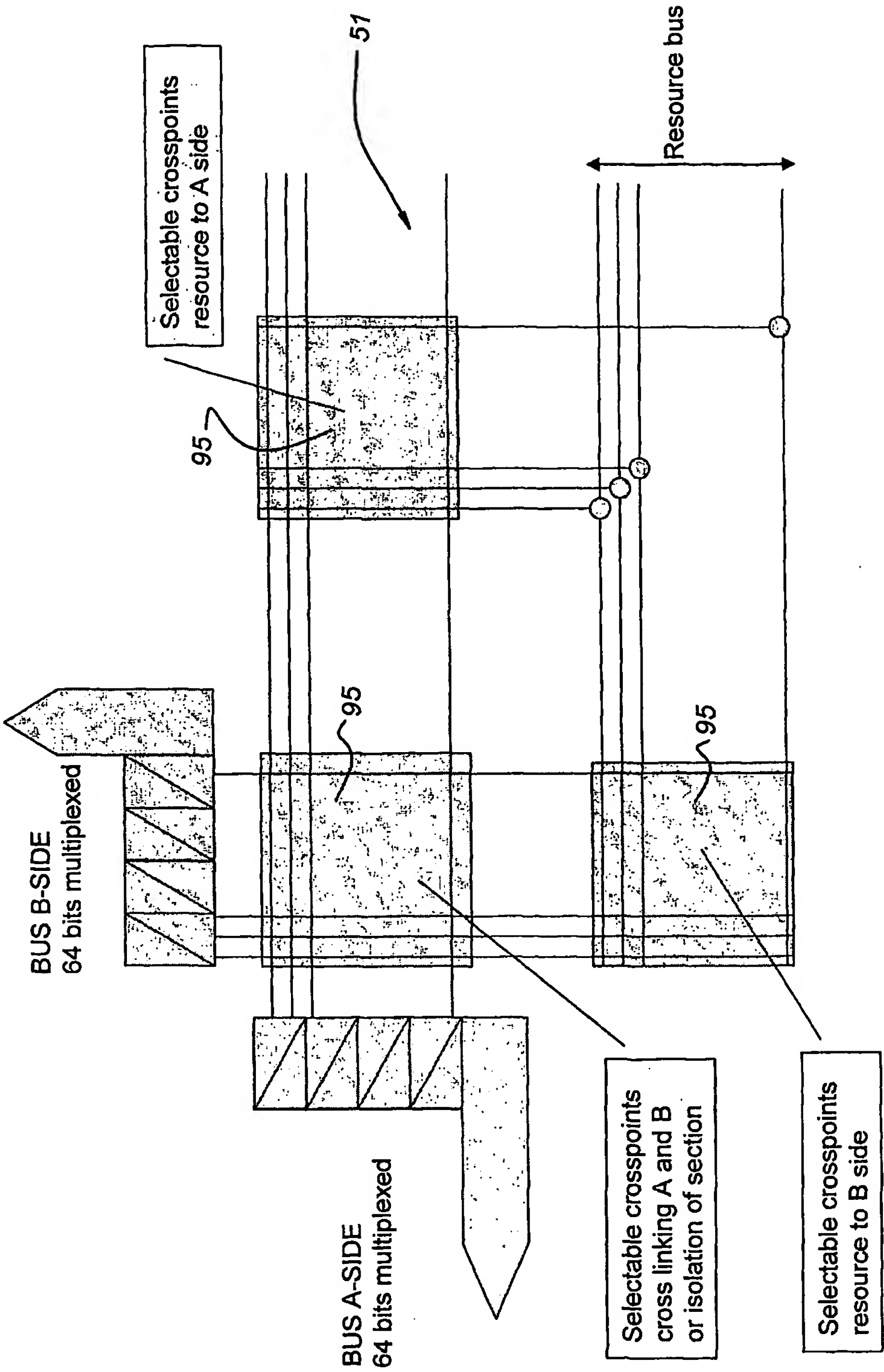


Fig 15

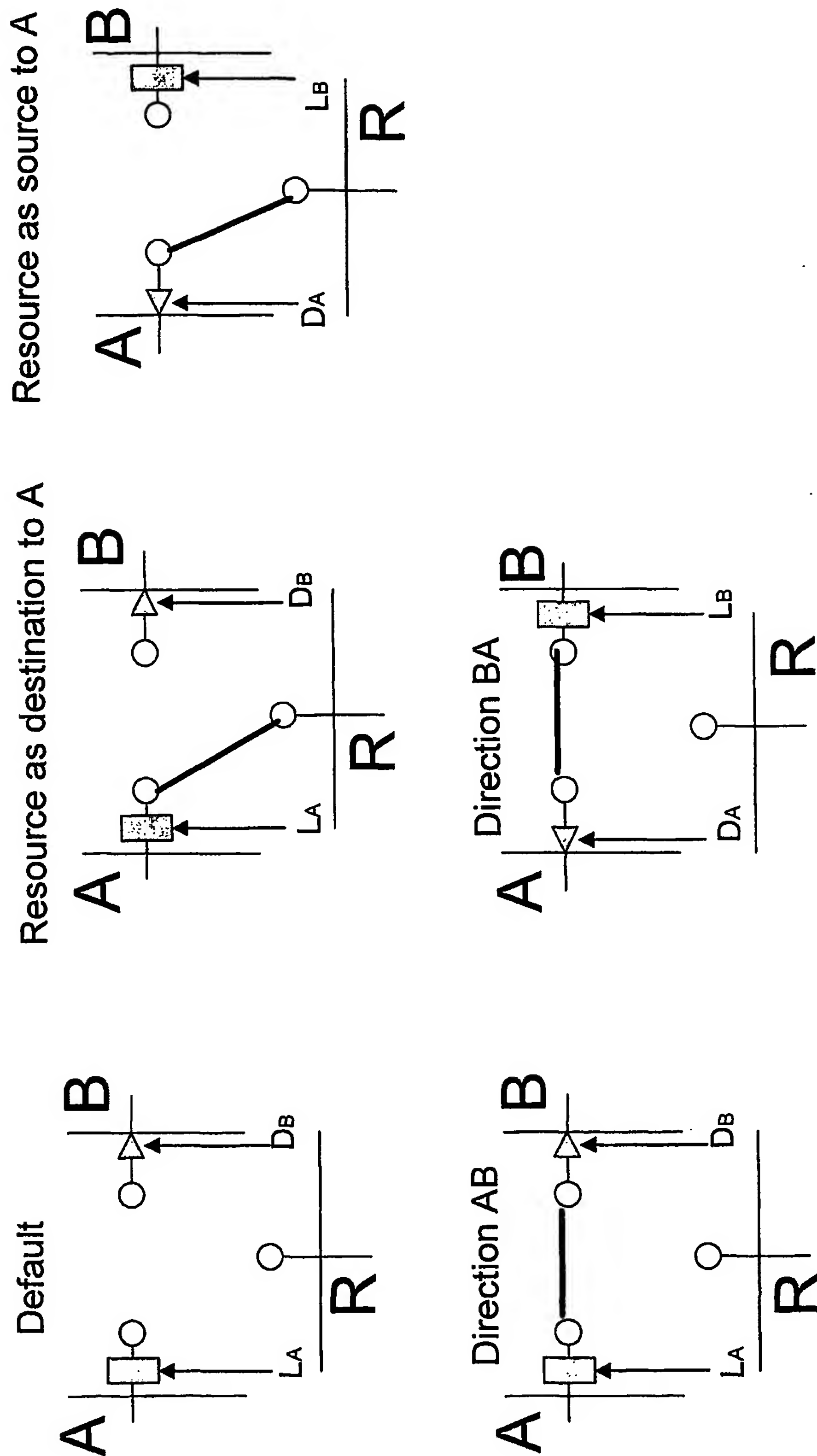


Fig 16a

51

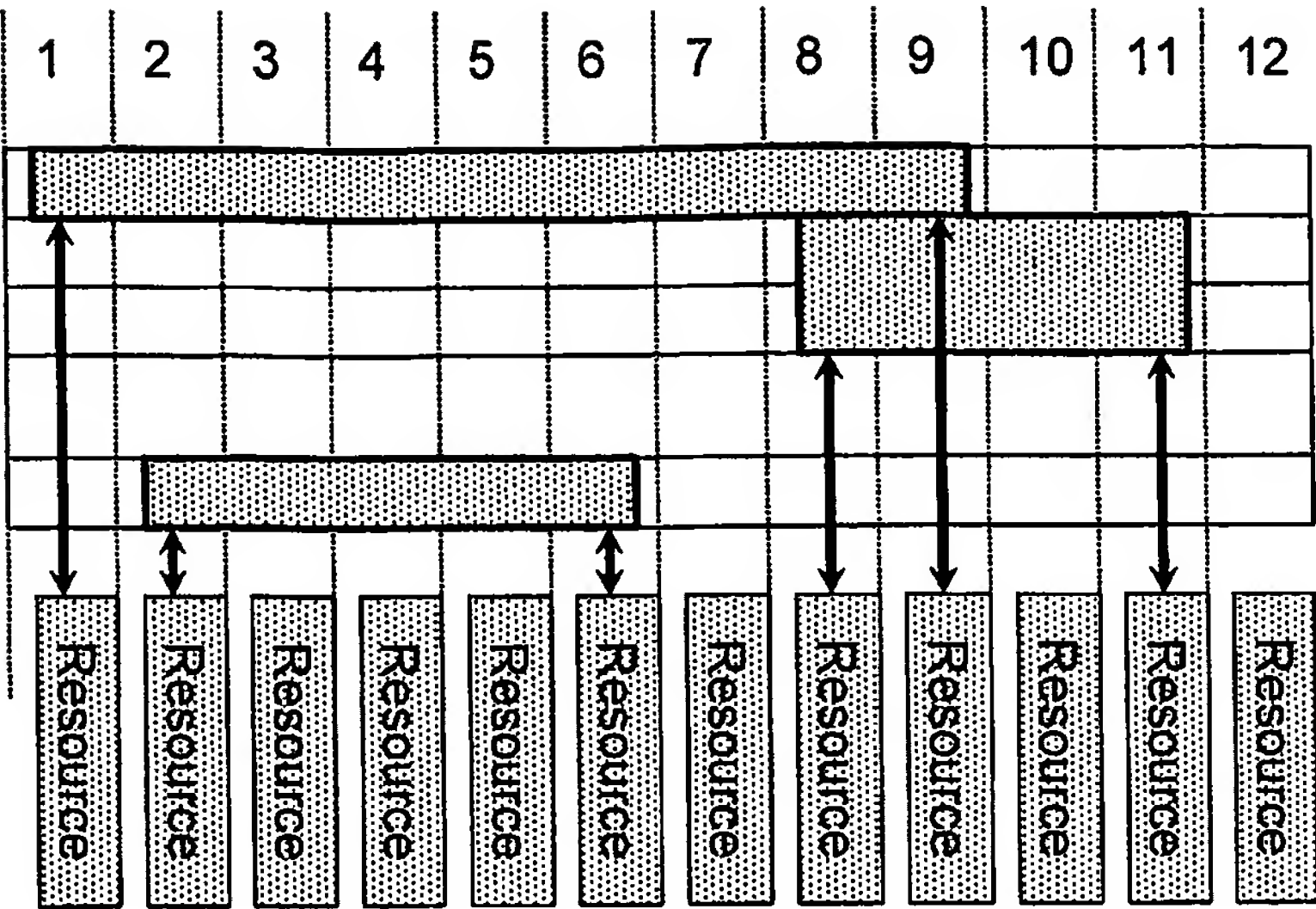


Fig 16b

51

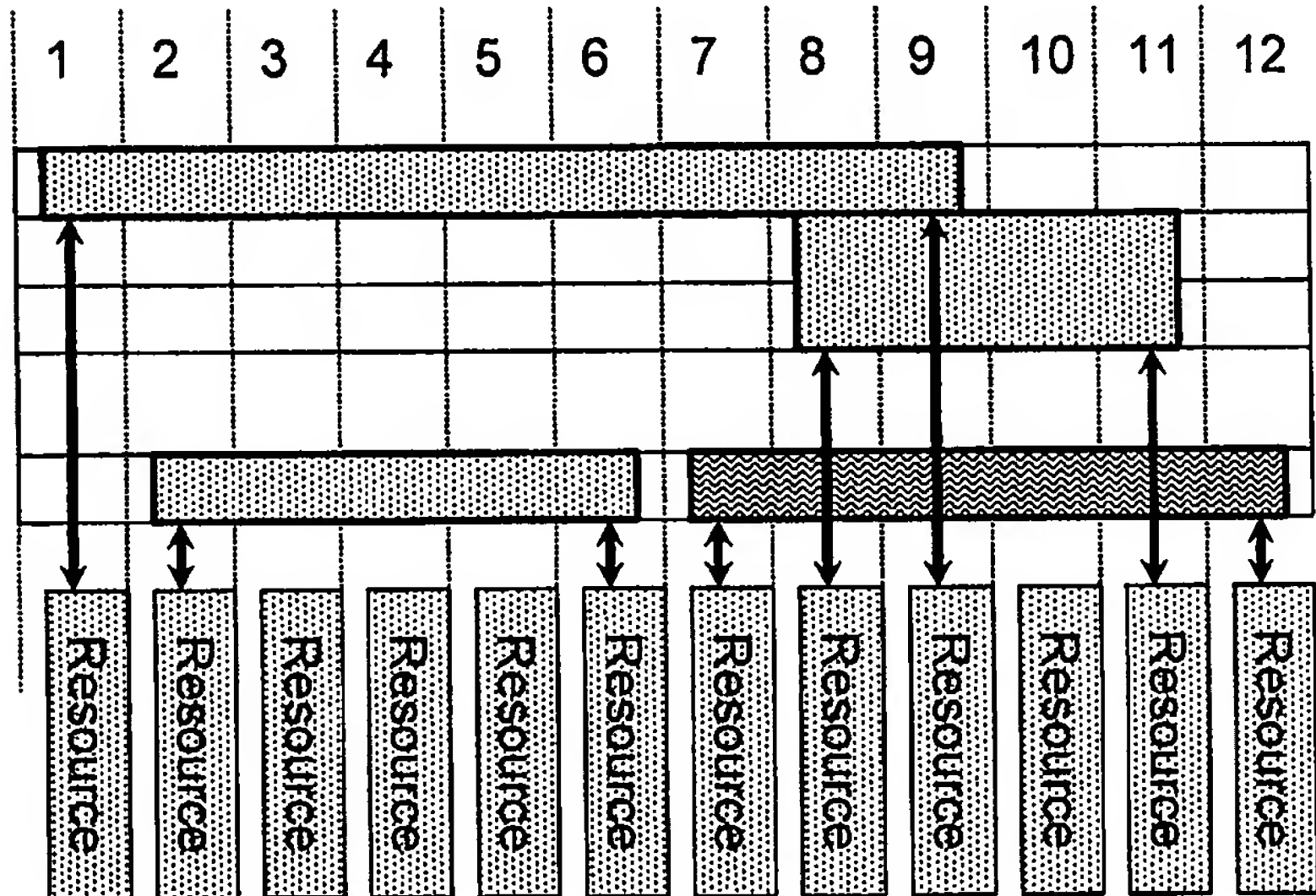


Fig 16c

51

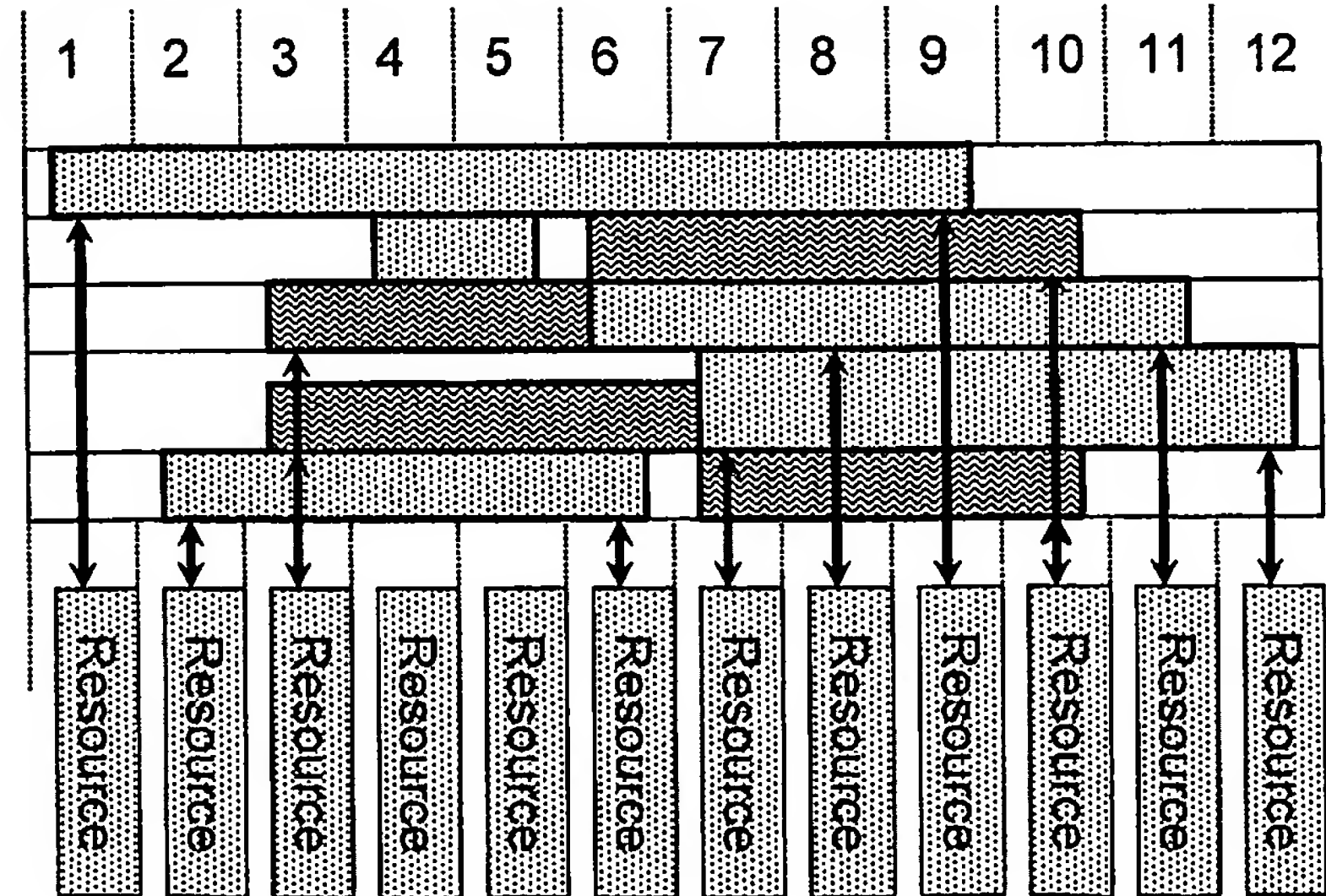
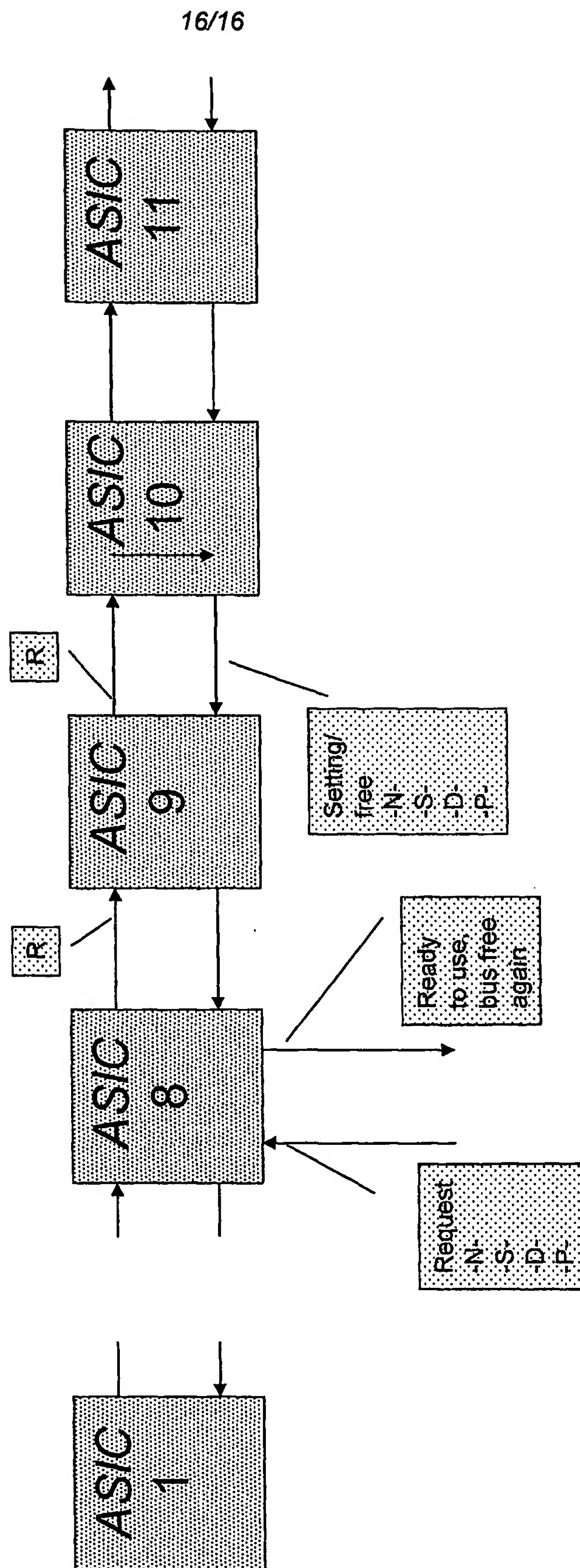


Fig 17



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